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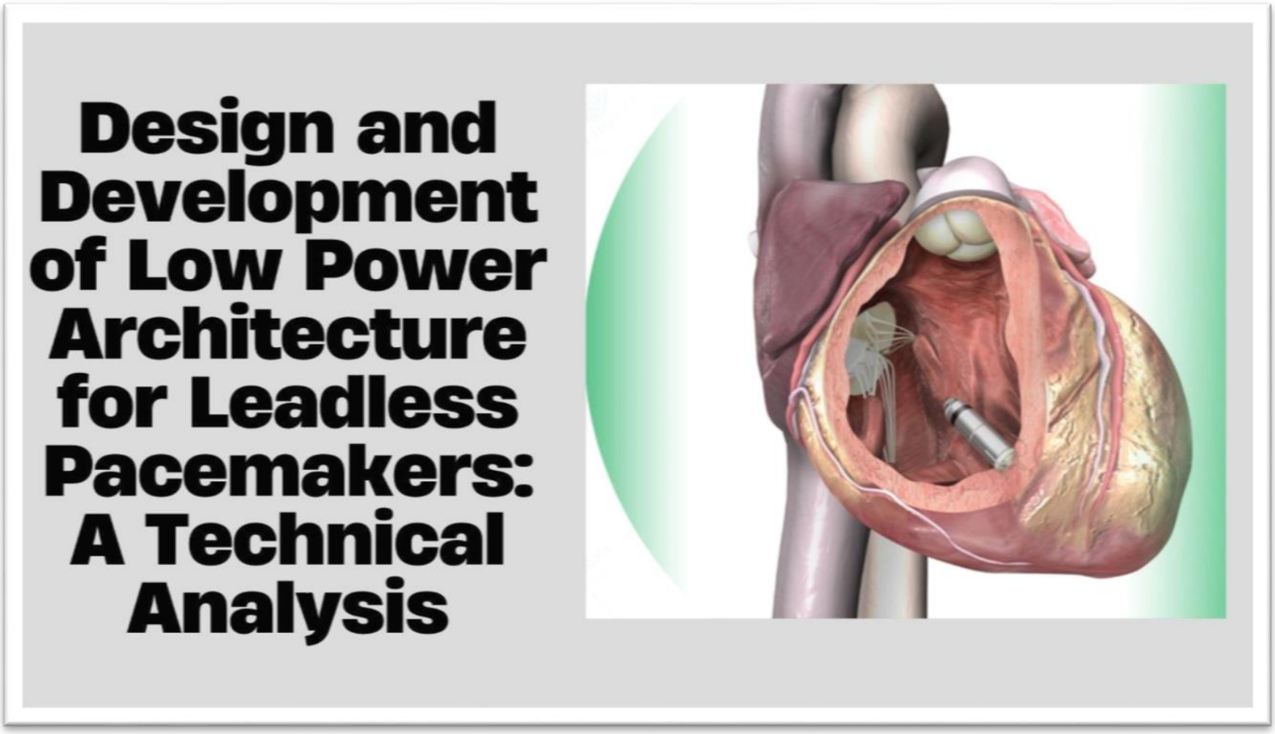
Design and Development of Low Power Architecture for Leadless Pacemakers: A Technical Analysis

Rajshaker Reddy Kankula

Abbott Laboratories



Abstract

The design and development of low-power electronics for implantable leadless pacemakers present significant engineering challenges due to strict power, size, and longevity constraints. This article explores key aspects of low-power circuit design, power management, and energy-efficient communication to enhance the operational lifespan of these devices. We discuss power constraints in leadless pacemakers, emphasizing energy efficiency strategies such as ultra-low-power microcontrollers, analog front-end design, and memory optimization. Power management architectures, including ultra-low quiescent current regulators, dynamic voltage scaling, and power gating, are analyzed to minimize energy consumption. Additionally, low-power wireless telemetry solutions, including Bluetooth Low Energy and MICS band communication, are evaluated for their impact on power efficiency. We also address low-power sensing and stimulation circuitry, optimizing ECG and intracardiac electrogram acquisition while maintaining effective pacing pulse generation. The potential of energy harvesting technologies, such as piezoelectric and electromagnetic methods, is explored as a complementary power source. Furthermore, biocompatibility and long-term reliability considerations are examined to ensure device longevity. Finally, emerging trends, including AI-driven power management and advanced semiconductor technologies, are discussed as future directions. This work provides a comprehensive overview of state-of-the-art low-power design strategies for leadless pacemakers, contributing to their advancement in clinical applications.

Keywords: Leadless Pacemakers, Energy Harvesting, Power Management, Artificial Intelligence Integration, Biomedical Implants, Transient Voltage Suppressors, Conductive System Pacing, Analog to Digital Converter

1. Introduction

Leadless pacemakers are a significant advancement in cardiac pacing technology. Unlike traditional pacemakers, which require leads (wires) to connect the pulse generator to the heart, leadless pacemakers are self-contained units implanted directly into the ventricle/atrial chambers of the heart via a catheter inserted through the femoral vein. These devices are much smaller, typically about 3-4 cm in length, and eliminate the need for a separate battery or subcutaneous pocket. Leadless pacemakers offer several benefits, including reduced risk of infection, lead displacement, and other complications associated with traditional pacemakers.

Lower power electronics are crucial for the longevity and reliability of implantable leadless pacemakers. These devices often rely on batteries for power, and reducing power consumption extends battery life, minimizing the need for invasive replacement surgeries. Additionally, lower power consumption reduces heat generation, which is vital for patient safety and comfort. Advances in low-power electronics enable the development of more compact and efficient leadless pacemakers, enhancing their functionality and patient outcomes. The top players in leadless pacemaker technology include:

- Medtronic: Known for its Micra AV2 and Micra VR2 leadless pacemakers, which are among the smallest pacemakers available [1].
- Abbott Laboratories: Recently launched the Aveir DR system, the first commercially available dual-chamber leadless pacemaker in the US [2].

Clinical studies conducted between 2018 and 2022 have demonstrated remarkable progress in leadless pacing technology, with wireless device communication achieving success rates of 98.8% across 912 interrogation attempts. The mean procedure duration for implantation has significantly improved to 28.3 ± 16.8 minutes, with a successful implant rate of 96%. These devices have shown exceptional performance metrics, maintaining consistent capture thresholds of 0.57 ± 0.31 V at 0.24 ms pulse width throughout the follow-up period, while R-wave amplitudes remained stable at 10.6 ± 5.2 mV. Battery longevity projections indicate an estimated service life of 4+ years at 100% ventricular pacing [3]. Contemporary research involving extensive patient cohorts has revealed compelling evidence supporting the efficacy of leadless cardiac pacemakers (LCPs). A comprehensive analysis of 2,817 patients with a mean age of 75.6 ± 11.4 years demonstrated successful device implantation in 99.1% of cases. The technology has shown particular promise in treating various cardiac conditions, with 66.2% of patients receiving the device for permanent atrial fibrillation with atrioventricular block, 15.6% for sinus node dysfunction, and 18.2% for other bradycardic conditions. The mean implantation duration has been optimized to 25.9 ± 15.4 minutes, with 92% of procedures completed within 60 minutes. Performance metrics have remained consistently impressive, with mean R-wave amplitudes of 11.2 ± 5.4 mV and capture thresholds stabilizing at 0.58 ±

0.31 V at 0.24 ms during long-term follow-up. The rate of major complications has been remarkably low at 1.51%, with device dislodgement occurring in only 0.42% of cases [3]. Figure 1 shows a typical internal block diagram of a leadless pacemaker.

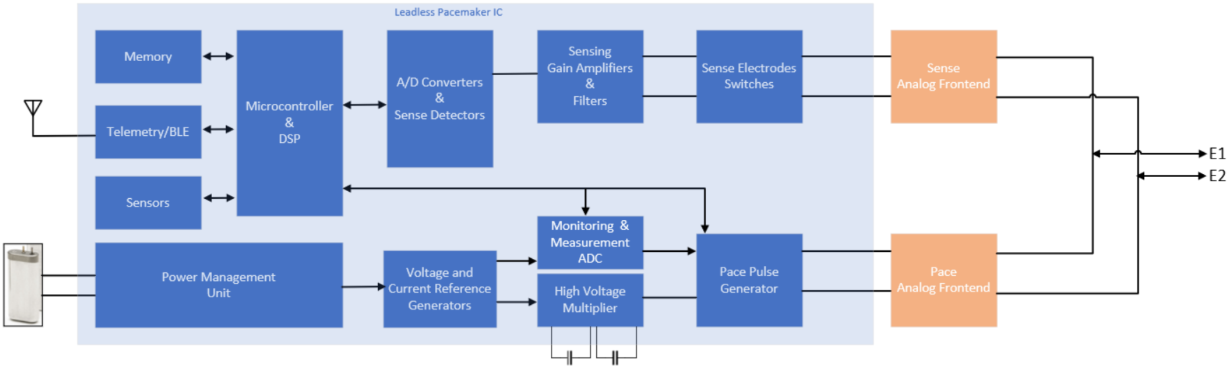


Figure 1: Leadless Pacemaker Internal Block Diagram [1,2]

2. Power Constraints and Energy Efficiency

Implantable leadless pacemakers must operate within stringent power constraints to ensure long-term functionality while maintaining a compact form factor. Given the limited energy storage capacity and the need for sustained operation over several years, optimizing power efficiency is paramount. This section explores key considerations in power budgeting, compares battery-powered operations with energy harvesting techniques, and provides a breakdown of power consumption across major subsystems.

2.1 Power Budget Considerations

Leadless pacemakers operate under strict energy limitations due to their small form factor and long operational lifetime, often exceeding 10 years. The power budget is dictated by various design constraints, including battery capacity, stimulation energy requirements, sensing circuitry power consumption, and wireless communication demands. Several factors influence power budgeting in leadless pacemakers:

- Device Miniaturization: Leadless pacemakers must be small enough for catheter-based implantation, limiting the available volume for the power source.
- Energy Storage Limitations: Typically, these devices rely on lithium-based primary batteries with a finite energy density, requiring ultra-low-power operation.
- Duty Cycle Considerations: Unlike continuous operation devices, pacemakers function intermittently, activating only when necessary. However, background processes such as sensing, telemetry, and self-checks contribute to overall power consumption.
- Thermal and Biocompatibility Constraints: Excessive power dissipation can lead to heat generation, which may affect surrounding tissue. Low-power design ensures safe thermal profiles.
- Trade-offs in Stimulation Strength and Frequency: Higher energy pulses ensure effective cardiac stimulation but reduce battery longevity, necessitating energy-efficient pacing algorithms.

Optimizing power efficiency in leadless pacemakers requires balancing these factors to extend device longevity without compromising performance.

2.2 Battery Powered Operation

Historically, leadless pacemakers have relied on battery-powered operations due to the high reliability and predictable energy output of primary lithium batteries. However, advancements in energy harvesting technologies have introduced alternative power sources that may extend device lifespan or even enable battery-free operation in the future.

- Lithium-Based Batteries: Most leadless pacemakers use lithium-carbon monofluoride (Li-CFx) or lithium-manganese dioxide (Li-MnO2) batteries, which offer high energy density and stability over time.
- Capacity Limitations: Given size constraints, battery capacities range from 20-50 mAh, requiring efficient power utilization to achieve a 8 to 10 year operational lifespan.
- Discharge Characteristics: The voltage profile of these batteries must be carefully managed to ensure stable performance over extended periods.
- End-of-Life Considerations: When the battery is depleted, device replacement is required, necessitating minimally invasive retrieval and reinsertion procedures.

City Labs has recently introduced NanoTritium batteries, which utilize the radioactive decay of tritium to generate consistent, low-level power. These betavoltaic batteries offer a lifespan exceeding 20 years, significantly outlasting traditional battery technologies [4]. Their compact size supports the miniaturization of leadless pacemakers, and their stable power output ensures reliable device performance over extended periods.

2.3 Power Consumption Breakdown of Major Subsystems

To optimize power efficiency, it is essential to understand the power distribution across major subsystems in a leadless pacemaker. The primary contributors to power consumption include:

Cardiac Stimulation Circuitry: This includes the pacing circuit that generates electrical impulses to stimulate the heart. The typical power consumption is 5-20 µJ per pulse, depending on pacing amplitude, pulse width, and lead impedance. Adaptive pacing algorithms, lower output voltages, and energy-efficient capacitor discharge circuits are some of the important aspects that should be looked into for power optimization.

Cardiac Sensing and Analog Front-End (AFE): Continuous monitoring of cardiac activity (ECG) requires ultra-low-power analog front-end circuits. The typical power consumption of this block is 1-10 µW, influenced by amplification, filtering, and ADC conversion. Duty-cycled sensing, low-power amplifiers, and efficient signal processing are some of the power optimization areas.

Wireless Telemetry and Communication: Wireless telemetry is essential for device interrogation, programming, and health monitoring. The typical power consumption of the wireless telemetry and communication circuits is 50-500 µW depending on transmission frequency and duration. Burst-mode transmission, low-power RF protocols (e.g., MICS band, Bluetooth Low Energy) are some of the research areas for power optimization in this domain.

Power Management Unit (PMU): Efficient power conversion and regulation are critical for stable operation of leadless pacemakers. Typical power lost in generating the internal voltages through voltage regulators and DC-DC converters for the pacemaker operation is around 5-50 µW. Using high efficiency voltage regulators and DC-DC converters help with reducing the power conversion loss.

Embedded Microcontroller and Memory: The microcontroller governs pacemaker operation, stores diagnostic data, and processes algorithms. It's the heart of the leadless pacemaker and consumes 1-100uW depending on computational load and operation duty cycling. Using low power sleep modes and energy efficient instruction sets can help reduce the power consumption and increase longevity.

Housekeeping Functions: In addition to pacing, sensing, power management, microcontroller and memory related functions, certain hardware blocks such as watchdog and RTT(real time clock) are needed to perform periodic system checks and ensure reliability. The typical power consumption of these blocks is <10 µW, but contributes to overall energy drain. Optimized scheduling of self-tests and efficient watchdog timers can be implemented to reduce the net power consumption. Table 1 provides a summary of the power consumption of the major subsystems within the leadless pacemakers.

Power constraints in leadless pacemakers necessitate a highly optimized design approach to ensure longevity and reliability. A well-balanced power budget, combined with advanced power management techniques, can significantly enhance device lifespan. While battery-powered operation remains the dominant energy source, emerging energy harvesting technologies may complement or replace batteries in the future. Understanding power consumption breakdowns across subsystems is crucial for designing next-generation ultra-low-power leadless pacemakers.

Subsystem	Power Consumption	Optimization Strategies
Cardiac Stimulation	5-20 uJ per pulse	Adaptive pacing, efficient discharge circuits
Sensing & AFE	1-10uW	Duty cycling, low power amplifiers
Wireless Telemetry	50-500uW	Burst transmission, low power RF
Power Management Unit	5-50uW	High efficiency regulators, dynamic voltage scaling
Microcontroller and Memory	1-100uW	Sleep modes, efficiency instruction set, RAM retention
Housekeeping Functions	<10uW	Optimized self-tests, watchdog timers, RTT

Table 1: Power Consumption of Major Subsystems in Leadless Pacemaker

3. Low Power Circuit Design Techniques

3.1 Ultra Low Power Microcontroller and DSP

In a leadless pacemaker, the microcontroller (MCU) performs several critical activities to ensure the device functions effectively and efficiently. Here are the key activities:

- **Sensing Cardiac Activity:** The MCU continuously monitors the electrical activity of the heart to detect arrhythmias or irregular heartbeats. This real-time sensing is crucial for determining when pacing is needed.
- **Generating Pacing Pulses:** Based on the sensed cardiac activity, the MCU generates electrical pulses to stimulate the heart when it detects a slow or irregular heartbeat. These pulses help maintain a regular heart rhythm.

- **Data Processing and Storage:** The MCU processes the data collected from the heart's electrical signals and stores it for later retrieval. This data can be used by healthcare providers to analyze the patient's heart activity and adjust the pacemaker settings if necessary.
- **Communication:** The MCU facilitates communication with external devices, such as programmers or monitoring systems. This allows healthcare providers to adjust the pacemaker settings wirelessly and monitor the device's performance.
- **Power Management:** Efficient power management is crucial for extending the battery life of the pacemaker. The MCU manages power consumption by switching between active and low-power modes as needed.
- **Safety and Reliability:** The MCU ensures the safety and reliability of the pacemaker by performing self-checks and diagnostics. It can detect and respond to potential malfunctions or abnormalities in the device.

Recent advancements in biotechnology and microelectronics have significantly impacted the design and development of leadless pacemakers. These devices, which are crucial for managing cardiac arrhythmias, require ultra-low power consumption to extend battery life and ensure long-term functionality. The integration of ultra-low power microcontrollers (MCUs) and digital signal processors (DSPs) is pivotal in achieving these goals.

One notable example is the use of the Cortex-M0+ MCU, which has been benchmarked for its ultra-low power operations. Research indicates that the Cortex-M0+ can achieve average leakage power consumption as low as 136 nW using CMOS065 (65nm) technology1. Additionally, the active power consumption is approximately 49.9 µW/MHz, making it highly suitable for continuous real-time heart activity monitoring [5]. Similarly, the Texas Instruments C5000 DSP family is designed for low-power applications, offering features such as power domain and operation voltage control, and efficient clock management [5]. These DSPs are ideal for portable medical devices, providing the necessary computational power while maintaining low energy consumption. The SimpleLink MSP432 microcontrollers also contribute to ultra-low power designs with their lower active power consumption and efficient low-power modes [6]. These MCUs integrate power enhancements that reduce current consumption, making them suitable for leadless pacemakers.

In conclusion, the development of leadless pacemakers relies heavily on the integration of ultra-low power MCUs and DSPs. Technologies such as the Cortex-M0+, C5000 DSP, and SimpleLink MSP432 play a crucial role in extending battery life and ensuring the reliable performance of these life-saving devices.

3.2 Analog Front-End

In leadless pacemakers, the analog front end (AFE) is a crucial component responsible for interfacing with the heart's electrical signals. The analog front end typically includes several key elements:

Electrodes: The electrodes are the physical interface between the pacemaker and the heart tissue. They detect the electrical activity of the heart and deliver pacing pulses when needed. The materials used in pacemaker leads significantly impact power consumption, though indirectly. The electrode material's conductivity directly affects how efficiently electrical impulses travel through the lead. Materials with higher conductivity minimize energy loss during transmission. This means the pacemaker needs to expend less power to deliver the required stimulation. The insulating materials surrounding the conductive wires are crucial. Effective insulation prevents current leakage, which would waste energy. Materials that maintain their insulating properties over long periods within the body are essential. In addition, lead materials can influence the "pacing threshold," which is the minimum amount of electrical energy required to stimulate the heart. Materials that promote a lower pacing threshold reduce the pacemaker's power demand. The most common materials used as tissue contact material are cobalt-chromium-molybdenum(CoCrMo) alloy and Platinum alloy because of their excellent conductivity and biocompatibility properties.

Protection Networks: Protection networks within the AFE of a leadless pacemaker are crucial for safeguarding sensitive circuits from high voltage defibrillation pulses and external electromagnetic interference in addition to ensuring patient safety and improving signal integrity. The most common components used in protection networks of leadless pacemakers are:

- Diodes: These are crucial for clamping voltages, preventing excessive voltage spikes from reaching sensitive circuitry. Schottky diodes known for their low forward voltage
- Resistors: Resistors limit current flow, protecting against overcurrent situations. High-value resistors should be used wherever possible to minimize current draw.
- Capacitors: Capacitors are used for filtering noise and transient voltages. They also play a role in energy storage and discharge during protection events.
- Transient Voltage Suppressors (TVS): These components are designed to quickly absorb high-voltage transients, providing robust protection against defibrillation pulses and other electrical surges.

Selecting components with extremely low leakage currents is essential. This minimizes the amount of current that flows through the protection network when it's not actively protecting the device. Utilizing advanced semiconductor fabrication techniques enables the creation of diodes and TVS devices with lower forward voltage drops and leakage currents. Further, reducing parasitic capacitance and inductance within the protection network is crucial for minimizing energy losses. Careful layout and component selection are essential.

Amplifiers: These are used in sensing analog front end to amplify the tiny electrical signals generated by the heart so that they can be brought back into the ADC's dynamic range. The signals from the heart are usually in the microvolt range and need amplification to be useful. Implementation of a novel chopper-stabilized biopotential amplifier architecture has demonstrated exceptional performance metrics, with a measured input-referred noise of 0.98 µVrms across the 0.5-100 Hz bandwidth while consuming only 515 nW per channel. The system achieves a mid-band gain of 52 dB with a bandwidth extending from 0.2 Hz to 180 Hz, utilizing a supply voltage of 1.0V. Advanced circuit techniques incorporating auto-zeroing and dynamic element matching have reduced the DC offset to ±15 µV, while maintaining a common-mode rejection ratio (CMRR) of 110 dB and power supply rejection ratio (PSRR) of 95 dB [7].

Filters: Filters are employed to remove noise and unwanted frequencies from the heart's electrical signals. This ensures that only the relevant cardiac signals are processed, improving the accuracy of the pacemaker's sensing capabilities. Designing filters for low power consumption in leadless pacemakers requires careful consideration of several factors. The key strategies to keep in mind while designing filters are:

- Optimize Filter Topology: Choose filter topologies that inherently consume less power, such as switched-capacitor filters or passive RC filters. These topologies can be more power-efficient compared to active filters.

- Use Low-Power Operational Amplifiers: Select operational amplifiers (op-amps) that are specifically designed for low-power applications. These op-amps typically have lower quiescent current and can significantly reduce the overall power consumption of the filter.
- Dynamic Power Management: Implement dynamic power management techniques, such as power gating and clock gating, to turn off or reduce the power to the filter circuits when they are not in use.
- Efficient Biasing Techniques: Use efficient biasing techniques to minimize the power consumption of the active components in the filter. For example, sub-threshold biasing can be used to operate transistors at very low power levels.
- Low Supply Voltage: Design the filter to operate at the lowest possible supply voltage that still meets the performance requirements. Lowering the supply voltage reduces the power consumption quadratically.
- Adaptive Filtering: Implement adaptive filtering techniques that adjust the filter parameters in real-time based on the signal conditions. This can help reduce power consumption by optimizing the filter performance dynamically.

Analog-to-Digital Converters (ADCs): The ADCs convert the amplified and filtered analog signals into digital signals that can be processed by the microcontroller or DSP. This conversion is essential for the digital processing and analysis of the heart's electrical activity. Tang X, et al. [17] introduced a Dynamic Predictive Sampling (DPS) Analog-to-Digital Converter (ADC) tailored for sparse signal sensing, particularly in electrocardiogram (ECG) monitoring applications. This analog-to-digital conversion system employs an innovative successive approximation register (SAR) architecture optimized for cardiac signal acquisition. Measurement results demonstrate 12-bit resolution at 2 kS/s while consuming only 288 nW, achieving a figure of merit of 18.2 fJ/conversion-step. The implementation includes a programmable gain amplifier with four selectable gain settings (20/40/60/80 dB), enabling dynamic range optimization based on input signal characteristics. Long-term stability testing under physiological conditions has shown remarkable performance, maintaining an effective number of bits (ENOB) of 10.8 over a temperature range of 35°C to 42°C, with total harmonic distortion (THD) remaining below -78 dB across the entire operating range.

3.3 Power Management Unit

The power management unit (PMU) in a leadless pacemaker is a critical component responsible for regulating and distributing power to the device's various subsystems. Given the stringent requirements for ultra-low power consumption, the PMU must generate precise and stable internal voltages. The key voltage requirements are:

Core Logic Voltage: This voltage powers the digital circuitry, including the microcontroller and signal processing units. It's typically a low voltage (e.g., 1.2V) to minimize power consumption.

Analog Circuitry Voltage: The analog front-end (AFE), which senses cardiac signals, requires a stable and low-noise voltage. This voltage may vary depending on the specific AFE design but typically stays at 1.2V and 3V.

Pacing Output Voltage: This voltage generates the electrical pulses that stimulate the heart. The required voltage amplitude varies depending on the pacing threshold and the patient's individual needs(typically <8V) and will need to be generated on the fly using charge pump networks. Generating the exact pace voltage needed using dynamic voltage regulation algorithms instead of using generic 1x, 2x and 3x battery voltage multiplier mode reduces the next power consumption.

Telemetry Voltage: When the pacemaker communicates wirelessly with external devices, a voltage is needed to power the telemetry circuitry. If the pacemaker integrates BLE to communicate with external world, 1.8V might be needed for IO communication with the microcontroller and 3V will be needed for telemetry communication.

Reference Voltages: The reference voltage is used to set the operating point of various analog circuits within the pacemaker, such as amplifiers and comparators. This ensures that these circuits operate within their optimal range, providing accurate signal processing while minimizing power consumption.

The latest generation of LDO's provided by Texas Instruments such as the TPS7A02 offer ultra low quiescent current (<25nA) while operating from an input voltage range of 1.5V to 6V and generating output voltage within the range of 0.8V to 5V making them highly suitable for implantable medical devices. These voltage regulators are capable of providing an efficiency of >95% [8].

DC-DC buck converters play a crucial role in implantable medical devices by efficiently managing power supply and ensuring device reliability. The latest offerings from TI such as TPS6220 synchronous buck converters offer 95% efficiency when operating in load current range of 100uA-1mA making them an excellent choice for implantable medical devices [9].

3.4 Energy Efficient Clocking and Timing Circuits

The design of energy-efficient clocking and timing circuits is crucial for the longevity and reliability of leadless pacemakers. These circuits are responsible for generating precise timing signals that control the pacing pulses and synchronize the device's operations with the heart's natural rhythm. One approach to achieving energy efficiency is the use of ultra-low power oscillators. These oscillators, such as MEMS-based oscillators, consume significantly less power compared to traditional crystal oscillators. MEMS oscillators can operate at power levels as low as 1 µW, making them ideal for implantable medical devices. Another technique involves dynamic frequency scaling (DFS), which adjusts the clock frequency based on the pacemaker's workload. By reducing the clock frequency during periods of low activity, the overall power consumption can be minimized. Additionally, duty cycling can be employed to turn off the clocking circuits when they are not needed, further reducing power usage. Advanced CMOS technologies, such as FD-SOI (Fully Depleted Silicon on Insulator), also contribute to energy-efficient designs. FD-SOI technology allows for lower operating voltages and reduced leakage currents, which are critical for extending battery life in leadless pacemakers [10].

The integration of ultra-low power oscillators, dynamic frequency scaling, duty cycling, and advanced CMOS technologies are key strategies for designing energy-efficient clocking and timing circuits in leadless pacemakers, ensuring their long-term functionality and reliability.

3.5 Low Power Memory and Data Storage Considerations

The design of low power memory and data storage systems is critical for the efficient operation of leadless pacemakers. These devices require reliable data storage to monitor and record cardiac activity while maintaining minimal power consumption to extend battery life. The types of memory storage units used in leadless pacemakers are:

ROM (Read-Only Memory): This type of memory stores the device's firmware, which is the essential software that controls the pacemaker's operation. This includes the pacing algorithms, sensing parameters, and communication protocols.

RAM (Random-Access Memory): RAM is used for temporary data storage during the pacemaker's operation. This includes storing real-time data from the cardiac sensors, temporary calculations, and buffer data for communication.

Non-Volatile Memory (e.g., Flash Memory): This type of memory is used for long-term storage of patient data, such as:

- Historical pacing data: Records of the heart's activity and the pacemaker's interventions.
- Diagnostic information: Data related to the device's performance and any detected abnormalities.
- Patient-specific settings: Customized pacing parameters and other configurations.

One key consideration is the use of non-volatile memory (NVM) for data storage. NVM, such as flash memory, retains data even when the power is turned off, making it ideal for storing critical patient data and device settings. Flash memory can be optimized for low power operation by using techniques such as wear leveling and error correction, which enhance its durability and reliability.

Random Access Memory (RAM) is also essential for the real-time processing of cardiac signals. Low power static RAM (SRAM) is preferred due to its fast access times and low power consumption. However, SRAM requires continuous power to retain data, making power management crucial. Techniques such as power gating and dynamic voltage scaling can be employed to reduce power consumption during idle periods.

RAM retention is another important aspect. Ensuring data retention in low power modes can be achieved by using retention latches and low leakage transistors. These components help maintain data integrity while minimizing power usage. Additionally, advanced CMOS technologies, such as FD-SOI (Fully Depleted Silicon on Insulator), can further reduce leakage currents and power consumption in memory circuits [11]. By integrating these technologies and techniques, leadless pacemakers can achieve efficient and reliable data storage, contributing to their long-term functionality and patient safety.

4. Wireless Communication and Telemetry

Leadless pacemakers represent a significant advancement in cardiac rhythm management, offering a minimally invasive alternative to traditional pacemakers. Unlike conventional pacemakers that rely on transvenous leads to deliver electrical pulses, leadless pacemakers are self-contained devices implanted directly into the right ventricle. A critical component of these devices is their wireless communication and telemetry systems, which enable real-time monitoring, data transmission, and remote reprogramming. This article explores the necessity of wireless communication and telemetry in leadless pacemakers and the techniques used to ensure low power consumption.

Wireless communication and telemetry play a crucial role in the effective operation of leadless pacemakers. Several factors necessitate their integration into these devices:

- Remote Monitoring and Diagnostics: Physicians require continuous or periodic access to pacing data to monitor device performance, battery status, and potential malfunctions. Wireless telemetry allows real-time transmission of patient data to healthcare providers, reducing the need for frequent in-person evaluations.
- Device Reprogramming: In some cases, pacemaker parameters such as pacing rate and output need to be adjusted based on patient needs. Wireless programming eliminates the need for surgical intervention and enhances patient safety.

- Data Logging and Analysis: Telemetry enables the storage and retrieval of critical event data, including arrhythmia episodes and battery life estimates. This information is vital for improving therapeutic outcomes and making informed clinical decisions.

  Given the small size and limited battery capacity of leadless pacemakers, optimizing power consumption is essential for ensuring longevity and reliability. Several techniques can be employed to achieve this:
- Ultra-Low Power Communication Protocols: Leadless pacemakers utilize low-power communication protocols such as Bluetooth Low Energy (BLE) or Medical Implant Communication Service (MICS, operating at 402–405 MHz). These protocols minimize energy expenditure while ensuring reliable data transmission.

  BLE devices periodically transmit advertising packets to announce their presence and establish connections. The frequency of these advertisements significantly impacts power consumption in LPs (Leadless Pacemaker). Shorter Advertising Interval (High Frequency) results in faster device discovery and lower latency in establishing a connection while the drawback is that it increases power consumption since the pacemaker transmits more frequently. Longer Advertising Interval (Low Frequency) reduces power consumption by minimizing the number of transmitted packets but will increase the time required for an external device (e.g., programmer or remote monitoring unit) to detect the pacemaker. For LPs, a compromise is typically reached where advertising intervals are dynamically adjusted based on patient needs. A duty-cycled approach, where the pacemaker increases advertising frequency only when necessary (e.g., during a follow-up session), helps extend battery life.
- Duty Cycling and Event-Driven Transmission: To conserve energy, wireless telemetry systems employ duty cycling, where the device remains in a low-power sleep mode and activates only when data transmission is required. Event-driven telemetry, where communication occurs only upon detecting clinically significant events, further enhances battery efficiency.
- Inductive Coupling and Energy Harvesting: Some leadless pacemakers use inductive coupling for short-range communication, which requires minimal power. Emerging research explores energy harvesting from cardiac motion or surrounding physiological signals to supplement battery power and extend device longevity.
- Adaptive Transmission Power Control: By dynamically adjusting the transmission power based on the proximity of the external receiver, leadless pacemakers minimize unnecessary energy expenditure, thereby optimizing power consumption.
- Data Compression and Efficient Encoding: Efficient data compression algorithms reduce the volume of transmitted data, decreasing the energy required for wireless communication. Encoding schemes such as differential encoding further enhance transmission efficiency.

Abbott Laboratories use a proprietary communication protocol using body conduction to program and communicate with AVEIR leadless pacemakers [12]. Using proprietary protocols help design the advertising schemes and communication schemes from the ground up thereby helping build ultra low power communication interfaces.

5. Pacing and Sensing Circuitry

Leadless pacemakers have revolutionized cardiac rhythm management by eliminating the need for transvenous leads, reducing infection risks, and simplifying implantation procedures. A core component of these devices is their pacing and sensing circuitry, which ensures reliable cardiac stimulation while detecting intrinsic cardiac activity. Due to the size constraints and limited battery capacity of leadless pacemakers, ultra-low power consumption is critical for extending device longevity. This article explores the functionality of pacing and sensing circuits and techniques for achieving low-power operation.

Pacing Circuitry: The primary function of the pacing circuitry is to deliver electrical pulses to the myocardium when the heart fails to generate adequate intrinsic beats. The pacing system includes:

- Pulse Generator: Generates an electrical impulse with controlled amplitude, pulse width, and frequency.
- Electrodes: Located on the pacemaker's surface to ensure direct contact with the endocardium.
- Energy Storage Capacitors: Store and release energy required for pacing stimulation.
- Charge Pump and Output Stage: Converts battery voltage to the required pacing output and ensures proper delivery of pacing pulses.
- Safety Features: Includes circuitry for detecting lead impedance abnormalities and preventing over-pacing.

When pacing is needed, the circuit delivers a pulse with a duration of 0.4 to 1.0 milliseconds and an amplitude ranging from 0.5V to 7V, ensuring effective myocardial capture while conserving energy. Given the battery constraints in leadless pacemakers, energy-efficient pulse generation needs to be used to minimize power consumption in pacing circuits. Charge balancing and low ESR capacitors can be used to optimize pulse energy delivery. Additionally, Adaptive pacing output algorithms that dynamically adjusts voltage and pulse width based on sense threshold measurements can be used to prevent excessive energy use.

Sensing Circuitry: Sensing circuits detect the heart's intrinsic electrical activity, allowing the device to determine if pacing is necessary. The sensing system includes:

- Electrodes: Used for both pacing and sensing, detecting the heart’s electrical signals.
- Amplifier: Boosts weak cardiac signals (~1 mV) while minimizing noise interference.
- Analog-to-Digital Converter (ADC): Converts analog signals into digital data for processing.
- Filtering and Signal Processing Unit: Differentiates between intrinsic heartbeats and external noise.
- Sensitivity Trigger Adjustment: Dynamically adjusts sensing thresholds to prevent inappropriate inhibition or triggering of pacing.
- Wide band EGM: Wideband EGM is captured for remote monitoring of electrocardiograms.

Proper sensing is essential to avoid unnecessary pacing, which can lead to battery depletion and arrhythmia mismanagement. Implementation of low-power operational amplifiers (op-amps) with nanoampere-level bias currents in addition to designing low-noise, high-impedance input circuits reduce the need for high-power amplification thereby reducing power consumption. Threshold-based wake-up logic, where the processor remains in ultra-low power mode until an event (e.g., intrinsic beat) is detected and use of asynchronous event detection instead of continuous sampling, significantly reducing ADC power consumption.

7. Biocompatibility and Reliability Considerations

Leadless pacemakers represent a significant advancement in cardiac rhythm management, offering a less invasive alternative to traditional transvenous systems. However, ensuring biocompatibility and long-term reliability remains paramount. The device's outer casing and components in direct contact with cardiac tissue must be composed of biocompatible materials. The performance and functionality of pacemakers depend upon the energy required to pace the heart muscle tissue, which is a function of programmed pulse width/amplitude and the voltage delivered between the electrodes, i.e., the anode and the cathode. This is clinically relevant because optimizing the pulse width and amplitude can significantly affect current drain and battery longevity of the device. It is the electrodes that provide this pivotal function in a pacemaker. Electrodes are metallic conductors that mediate the transition from electron flow in the electrode to ion/ionic flow in the tissue via reactions at the electrode-tissue interface [13].

When choosing materials and surface technologies for the electrodes, characteristics including material properties, surface structure and microstructure, electrochemical properties and charge transfer have to be considered. The surface must be able to transfer the electrical stimulation and also sense the cardiac response. Titanium alloys, known for their corrosion resistance and tissue compatibility, are frequently utilized. Polymeric coatings can further enhance biocompatibility, minimizing inflammatory responses and promoting tissue integration. Careful attention to surface finish and device geometry is crucial to reduce the risk of thrombus formation and tissue irritation.

Ensuring long-term reliability and avoiding premature battery depletion remain critical design and testing considerations for leadless pacemakers. One of the key factors influencing leadless pacemaker longevity is power efficiency. Optimizing battery life requires advancements in low-power circuit design, energy-efficient pacing algorithms, and the use of high-capacity lithium-based batteries. Additionally, adaptive pacing algorithms that adjust pacing output based on physiological demand help minimize energy consumption. Minimizing quiescent current drain and improving capacitor performance also contribute to prolonged device life.

Reliability is further enhanced through rigorous accelerated aging tests and adherence to regulatory standards. The International Organization for Standardization (ISO) and the International Electrotechnical Commission (IEC) provide testing frameworks for implantable medical devices. ISO 14708-1 and ISO 14708-2 outline performance and safety requirements, while IEC 60601-1 and IEC 60601-2-27 address general electrical safety and functional testing. Battery longevity is specifically evaluated through accelerated aging studies and telemetry-based monitoring of real-world performance.

Environmental stress testing, including thermal cycling, humidity exposure, and mechanical shock tests, ensures the device withstands physiological conditions. Electromagnetic compatibility (EMC) testing, per IEC 60601-1-2, confirms resistance to external electromagnetic interference. In addition to laboratory testing, real-world clinical data from post-market surveillance and registry studies provide crucial insights into long-term reliability. Analysis of explanted devices can reveal trends in battery depletion, confirming predicted performance.

Ensuring the biocompatibility and reliability of leadless pacemakers necessitates a multi-faceted approach, combining innovative design, rigorous testing, and continuous monitoring. By adhering to established medical device standards and advancing battery technology, manufacturers can enhance the longevity of leadless pacemakers, ultimately improving patient outcomes and reducing the need for device replacement procedures.

8. Future Trends and Emerging Technologies

Future advancements in LCP technology are poised to address current limitations and expand clinical applications, driven by emerging trends in energy harvesting, artificial intelligence (AI), and advanced materials.

One pivotal area of development lies in enhancing energy autonomy. Current LCPs rely on batteries with finite lifespans, necessitating replacement procedures. Future iterations will likely incorporate energy harvesting mechanisms to extend or eliminate battery dependence. Piezoelectric materials, converting mechanical energy from myocardial contraction into electrical energy, present a promising avenue [16]. Research is also exploring radiofrequency energy transfer, enabling external charging or powering of the device. Furthermore, advancements in miniaturized thermoelectric generators, leveraging the temperature differential between the blood and surrounding tissue, could provide a sustainable energy source.

AI-driven signal processing is another crucial frontier. Current LCPs primarily detect and pace the ventricle. However, integrating AI algorithms will enable more sophisticated signal analysis, facilitating personalized pacing strategies and improved diagnostic capabilities. AI can be trained to recognize subtle changes in intracardiac electrograms, predicting impending arrhythmias or heart failure exacerbations. This could allow for proactive interventions, reducing the need for hospitalization. Furthermore, AI-powered algorithms can optimize pacing parameters in real-time, adapting to individual patient needs and activity levels, thereby maximizing device efficacy and patient comfort. A novel deep-learning architecture designed specifically for cardiac rhythm classification has demonstrated exceptional performance while maintaining ultra-low power operation. The system employs a hybrid CNN-LSTM network structure implemented in 65nm CMOS technology, achieving 98.7% classification accuracy across five different arrhythmia types while consuming only 277.3 µW during inference. The network architecture utilizes 8-bit quantization with optimized weight distribution, requiring only 158 KB of memory while maintaining a classification latency of 78 ms at 50 MHz operation frequency [14].

The machine learning subsystem incorporates sophisticated power management techniques through dynamic precision scaling. During normal sinus rhythm detection, the system operates in a reduced precision mode consuming 89.2 µW, automatically transitioning to full precision operation consuming 277.3 µW when irregular patterns are detected. The implementation includes a novel batch processing approach that achieves 95.6% energy efficiency improvement compared to sample-by-sample processing. Performance validation using the MIT-BIH arrhythmia database has demonstrated classification accuracies of 99.1% for normal sinus rhythm, 97.8% for atrial fibrillation, 96.9% for ventricular tachycardia, and 98.2% for bradycardia, while maintaining an average power consumption of 156.4 µW across a 24-hour monitoring period [14].

The development of advanced biocompatible materials is essential for enhancing LCP longevity and functionality. Next-generation LCPs will likely employ nanomaterials and bio-integrated electronics to improve device integration with cardiac tissue. This includes developing flexible and conductive polymers that mimic the mechanical properties of the myocardium, minimizing tissue damage and inflammation. Additionally, self-healing materials and drug-eluting coatings can mitigate fibrosis and infection risks, further extending device lifespan.

Beyond ventricular pacing and atrial pacing, future LCPs are expected to expand their capabilities to include dual chamber pacing and conduction system pacing (CSP) to improve the electrical conduction of the heart. This requires advancements in inter-device communication and sensing capabilities. Wireless communication protocols, such as ultra-wideband technology, will facilitate seamless data exchange between multiple LCPs or between LCPs and external monitoring devices. This will enable more complex pacing algorithms, restoring physiological atrioventricular synchrony and improving hemodynamic performance.

Finally, the integration of remote monitoring and telemedicine will play a crucial role in optimizing LCP management. Continuous data transmission from the device to healthcare providers will enable proactive monitoring, early detection of complications, and remote adjustments of pacing parameters. This will improve patient outcomes, reduce the burden on healthcare systems, and enhance the overall patient experience.

9. Conclusion

The evolution of low-power architectures in leadless pacemakers has demonstrated remarkable progress through the integration of multiple innovative technologies and design approaches. From sophisticated power management systems to advanced battery chemistry innovations, these developments have significantly enhanced device longevity and reliability. The implementation of machine learning algorithms and efficient signal processing methods has further improved device performance while maintaining minimal power consumption. These advancements collectively represent a significant step forward in implantable cardiac device technology, promising improved patient outcomes and enhanced quality of life. The continued development of these technologies, especially in integrating AI to detect advanced arrhythmias, provide personal care, coupled with ongoing miniaturization efforts and energy harvesting technologies suggest a promising future for leadless pacemaker systems in cardiac care.

References

1. Medtronic “Meet Micra, our leadless pacemaker,” Medtronic.com. ”https://www.medtronic.com/en-us/l/patients/treatments-therapies/pacemakers/our/micra.html
2. Abbott “Revolutionizing Cardiac Care: Single & Dual Chamber Leadless Pacing.” https://www.cardiovascular.abbott/us/en/hcp/products/cardiac-rhythm-management/pacemakers/aveir-dr-dual-chamber-leadless-pacemaker-system.html?gad_source=1&gclid=Cj0KCQiAoJC-BhCSARIsAPhdfSiEX72jYfv7YMYzNfFsBZ2BV4E_NhQZqg2sxgt6rG14IaSFLTZ42rIaAk_TEALw_wcB&gclsrc=aw.ds
3. Richard G. Trohman., "Leadless Pacing: Current Status and Ongoing Developments," Section of Electrophysiology, Division of Cardiology, Department of Internal Medicine, Rush University

Medical Center, 1653 W. Congress, Chicago, IL 60612, USA, 2025. [Online]. Available: https://www.mdpi.com/2072-666X/16/1/89#:~:text=Wireless%20device%20communication%20was%20successful,Eight%20patients%20died.

4. City Labs “City Labs: Revolutionizing Long-Life Power Solutions.” https://citylabs.net/
5. NK Vasantha Kumar, “C5000 DSP-Based Low-Power System Design,” 2015. C5000 DSP-Based Low-Power System Design
6. Dung Dang et al., “Designing an Ultra-Low-Power (ULP) Application With
7. SimpleLink™ MSP432™ Microcontrollers,” 2017. Designing an Ultra-Low-Power (ULP) Application With SimpleLink™ MSP432™ Microcontrollers (Rev. A)
8. Lida Kouhalvandi et al., "Amplifiers in Biomedical Engineering: A Review from Application Perspectives," National Library of Medicine, 2023. [Online]. Available: https://pmc.ncbi.nlm.nih.gov/articles/PMC9961860/
9. Texas Instruments, “TPS7A02 Nanopower IQ, 25-nA, 200-mA, Low-Dropout Voltage Regulator With Fast Transient Response,” 2019. https://www.ti.com/lit/ds/symlink/tps7a02.pdf?ts=1741011711684
10. Texas Instruments “TPS6220x High-Efficiency, SOT23 Step-Down, DC-DC Converter,” 2015. https://www.ti.com/lit/ds/symlink/tps62200.pdf?ts=1741022597812&ref_url=https%253A%252F%252Fwww.google.com%252F
11. Nicolas Franzina et al., “A miniaturized endocardial electromagnetic energy harvester for leadless cardiac pacemakers,” 2020. https://journals.plos.org/plosone/article?id=10.1371/journal.pone.0239667
Nobuyuki Sugii, “Low-power-consumption fully depleted silicon-on-insulator technology,” Microelectronic Engineering, Volume 132, Pages 226-235. 2015. https://www.sciencedirect.com/science/article/abs/pii/S0167931714003360
12. Yvonne E. Kaptein et al., “Implant-to-implant diagnostics and programming of dual-chamber leadless pacemaker in an orthotopic heart transplant recipient,” Volume 9, Issue 11p811-814November 2023. https://www.heartrhythmcasereports.com/article/S2214-0271(23)00210-5/fulltext
13. Pulse Technologies “Hierarchically Restructured Titanium Electrodes: A Novel, Low-Cost, High-Performing Platform for Leadless Pacemakers.” Pulsetechnologies.com. https://pulsetechnologies.com/technical-blog/hierarchically-restructured-titanium-electrodes-a-novel-low-cost-high-performing-platform-for-leadless-pacemakers/#:~:text=Hierarchically%20Restructured%20Titanium%20Electrodes%3A%20A,for%20Leadless%20Pacemakers%20%2D%20Pulse%20Technologies
14. Abdul Hannan et al., “Machine learning-based smart wearable system for cardiac arrest monitoring using hybrid computing" Biomedical Signal Processing and Control, Volume 87, Part B, 105519, 2024. [Online]. Available: https://www.sciencedirect.com/science/article/pii/S1746809423009527
15. Newsroom, “Experimental pacemaker converts heartbeat energy to recharge battery,” 2023. https://newsroom.heart.org/news/experimental-pacemaker-converts-heartbeat-energy-to-recharge-battery

16. Majid Khazaee et al., “Conceptual Piezoelectric-Based Energy Harvester from In Vivo Heartbeats’ Cyclic Kinetic Motion for Leadless Intracardiac Pacemakers,” Micromachines (Basel). 2024 Sep 6;15(9):1133. https://pmc.ncbi.nlm.nih.gov/articles/PMC11434573/
17. Xiaochen Tang et al., “A Dynamic Predictive Sampling ADC for Sparse Signal Sensing in ECG Monitoring” arXiv preprint, 2022. [Online]. Available: https://arxiv.org/abs/2211.09901